

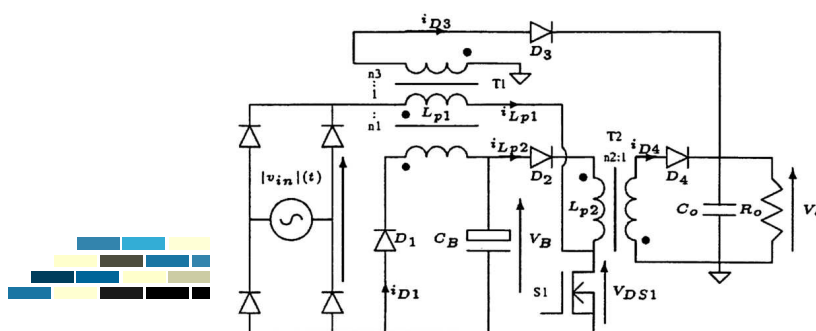
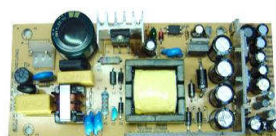
Postgraduate Scholarship on High-efficient Power Electronics Circuits Design

This scholarship is funded by an ARC Discovery-Projects grant and will provide support on a full-time basis for a research project leading to a Masters (support for 2 years) or a PhD (support for 3 years). The objective of the research project is to analysis, design and experiment a new family of high-efficient power electronics circuits for future practical use. Development of such circuits involves reduction of repeatedly input power processes, improvement in power factor and incorporating renewable energy sources.

Applicants should have an Honours 1 or 2A degree in electrical engineering or electronic engineering. Experience/background in power electronics and design of switching power converter would be an advantage. The scholarship stipend offered is \$20,007-\$21,000 per annum (tax exempt), dependent on the research capability and specific circumstances of the candidate. International students can apply but the scholarship does not cover any tuition fees payable by international students. It is required that the awardee of this scholarship will commence his or her research degree studies in Semester 2, 2008.

For further information, contact Dr. Dylan Lu (Phone: +61-2-9351 3496; Fax: +61-2-93513847; E-mail: dylan.lu@ee.usyd.edu.au). Applications, including a curriculum vitae, copy of an academic transcript, proof of citizenship or permanent residency, the names and contact details of at least two referees should be sent to Dr. Dylan Lu, School of Electrical and Information Engineering, The University of Sydney NSW 2006.

Closing Date: 21 July 2008



D. Lu, et al., US Patent #6,987,676